

Docket No. AUS9-2000-0572-US1

ABSTRACT OF THE DISCLOSURE

APPARATUS AND METHOD FOR AN ENHANCED INTEGER DIVIDE IN AN
IA64 ARCHITECTURE

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An apparatus and method for implementing a switch instruction in the IA64 architecture is provided. With the apparatus and method, a first register is used to identify whether a low is either 0, 1 or some other value, and a second register is used to identify a shift amount. The first register is then shifted by the shift amount in the second register. The first register value is then moved to the predicate register set in the IA64 architecture, thereby identifying which branch is to be taken. If the first register is shifted outside the predicate registers, a default address is provided.